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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE APPLICATION OF:

WILLIAM J. BORLAND ET AL.

CASE NO.: **EL0496 US NA**

APPLICATION NO.: **10/663,551**

CONFIRMATION NO.: **2580**

GROUP ART UNIT: **2831**

EXAMINER:

FILED: **SEPTEMBER 16, 2003**

FOR: **PRINTED WIRING BOARDS HAVING LOW INDUCTANCE EMBEDDED
CAPACITORS AND METHODS OF MAKING SAME**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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In compliance with 37 CFR 1.97 and 1.98, Applicant would like to bring to the attention of the U.S. Patent and Trademark Office information that may be helpful in the examination of the above-identified patent application. All of the information is listed on an attached Form PTO/SB/08.

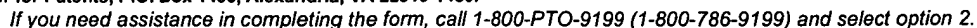
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Respectfully submitted,

REBECCA W. TULLOCH
ATTORNEY FOR APPLICANTS
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Facsimile: (302) 992-7343

Dated: 1/12/04

Enclosures





PTO/SB/08b (08-03)

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Application Number

10/663,551

Filing Date

September 16, 2003

First Named Inventor

William J. Borland

Art Unit

2831

Examiner Name

Attorney Docket Number

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NON PATENT LITERATURE DOCUMENTS

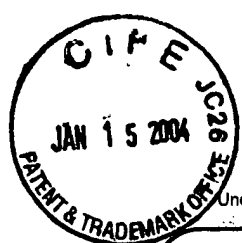
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		JOHN FELTEN AND SAUL FERGUSON, Embedded Ceramic Resistors and Capacitors for PWB, IP Printed Circuit Expo, San Diego, CA, April 2000.	<input type="checkbox"/>
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		JOHN J. FELTEN AND WILLIAM J. BORLAND, Embedded Ceramic Passives in PWB: Process Development, IPC Printed Circuit Expo, Anaheim, CA, April 2001.	<input type="checkbox"/>
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William J. Borland

Art Unit

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Examiner Name

Attorney Docket Number

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NON PATENT LITERATURE DOCUMENTS

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		WILLIAM BORLAND, JOHN J. FELTEN, Thick Film Ceramic Capacitors and Resistors inside Printed Circuit Boards, 34th International Symposium on Microelectronics (IMAPS), Oct. 9-11th, 2001, Baltimore, MD.	<input type="checkbox"/>
		JOHN FELTEN, RICHARD SNOGREN, JIMING ZHOU, Embedded Ceramic Resistors and Capacitors in PWB: Process and Performance, Fall IPC Meeting, October 11, 2001, Orlando, FL.	<input type="checkbox"/>
		WILLIAM J. BORLAND AND SAUL FERGUSON, Embedded Passive Components in Printed Wiring Boards, a Technology Review, To be published in CircuiTree Magazine, 2001.	<input type="checkbox"/>
		JIMING ZHOU, JOHN D. MYERS AND JOHN J. FELTEN, Embedded Passives Technology for PCBs: Materials, Design, and Process, IMAPS 2002 Conference, Denver, CO, Sept. 4-6, 2002.	<input type="checkbox"/>
		JOHN J. FELTEN, Electronic Circuits World Convention, Paper number IPC31, Advanced Embedded Passives Technologies - Putting Ceramic Components into Organic PWBs.	<input type="checkbox"/>
		WILLIAM BORLAND, JOHN J. FELTEN, SAUL FERGUSON, ALTON B. JONES AND ANGELA A. LAWRENCE, Embedded Singulated Ceramic Passives in Printed Wiring Boards, IMAPS Advanced Technology Workshop on Passive Integration, Ogunquit, ME, June 19-21, 2002.	<input type="checkbox"/>

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